

SOT-23 Plastic-Encapsulate Transistors
FEATURES

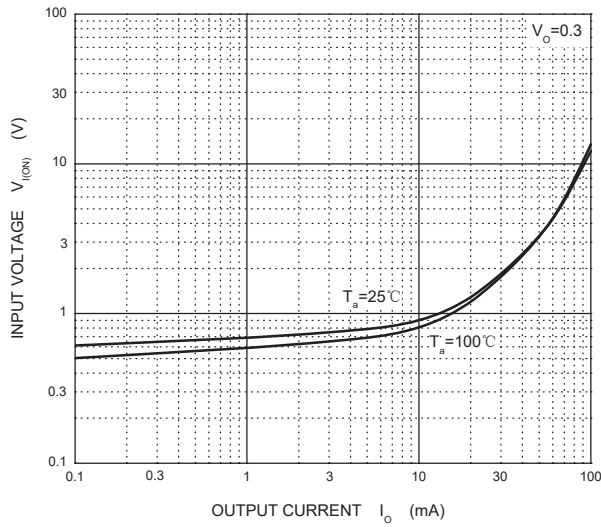
- Built-in bias resistors enable the configuration of an inverter circuit without connecting external input resistors(see equivalent circuit)
- The bias resistors consist of thin-film resistors with complete isolation to allow negative biasing of the input.They also have the advantage of almost completely eliminating parasitic effects
- Only the on/off conditions need to be set for operation, making device design easy

Parameter	Symbol	DTC143ZCA	Unit
Supply Voltage	V_{CC}	50	V
Input Voltage	V_{IN}	-5~+30	V
Output Current	I_O	100	mA
Power Dissipation	P_D	200	mW
Operation Junction and Storage Temperature Range	T_J, T_{stg}	-55~+150	°C

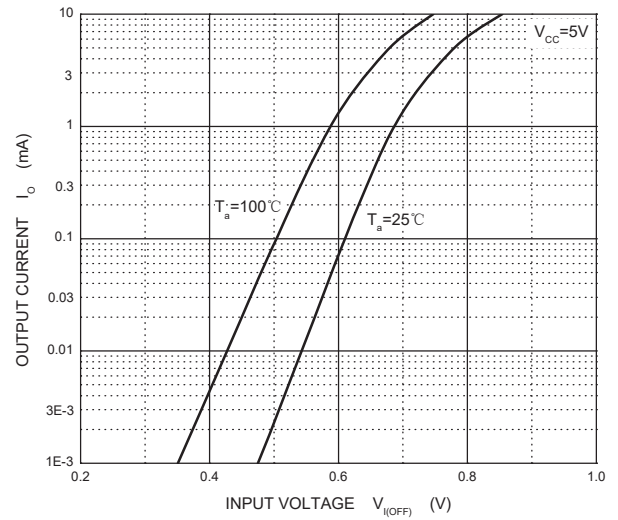
ELECTRICAL CHARACTERISTICS (Ta=25°C unless otherwise specified)

Parameter	Symbol	Conditions	Min	Typ	Max	Unit
Input voltage	$V_{I(off)}$	$V_{CC}=5V, I_O=100\mu A$	0.5			V
	$V_{I(on)}$	$V_O=0.3V, I_O=5mA$			1.3	V
Output voltage	$V_{O(on)}$	$I_O/I_I=5mA/0.25mA$		0.1	0.3	V
Input current	I_I	$V_I=5V$			1.8	mA
Output current	$I_{O(off)}$	$V_{CC}=50V, V_I=0$			0.5	μA
DC current gain	G_I	$V_O=5V, I_O=10mA$	80			
Input resistance	R_1		3.29	4.7	6.11	k Ω
Resistance ratio	R_2/R_1		8	10	12	
Transition frequency	f_T	$V_O=10V, I_O=5mA, f=100MHz$		250		MHz

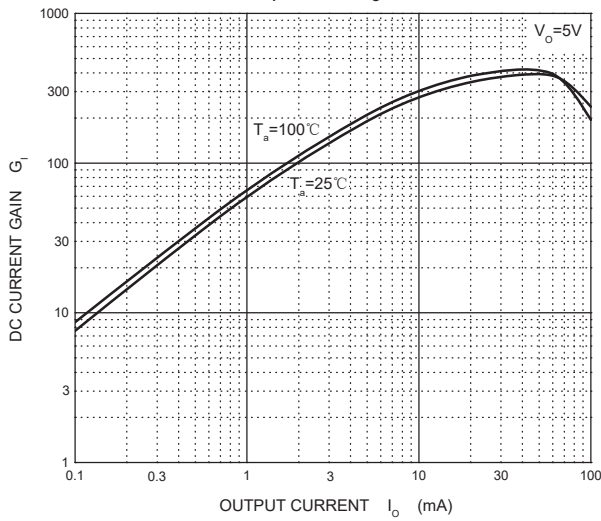
ON Characteristics



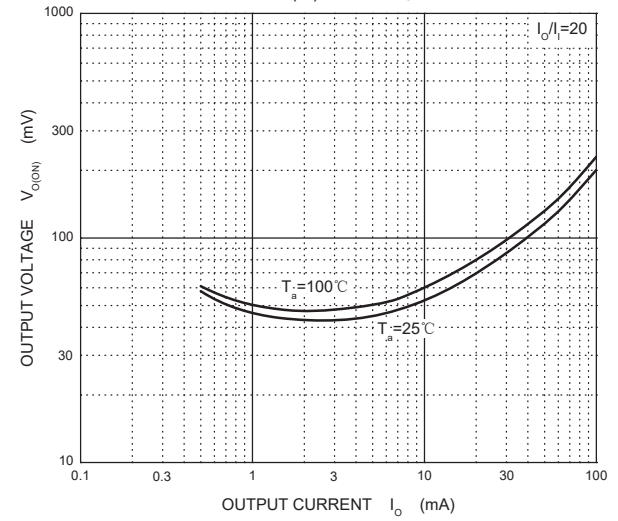
OFF Characteristics



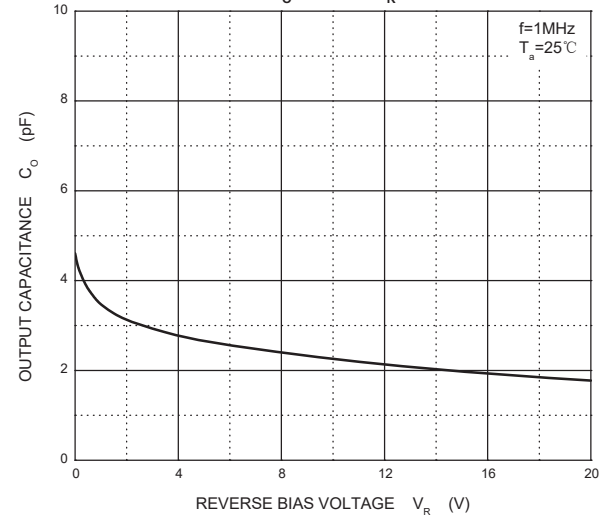
G_I — I_O



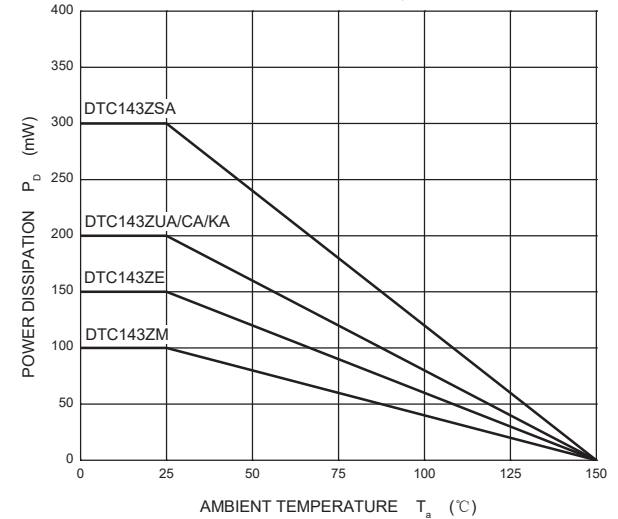
$V_{O(ON)}$ — I_O

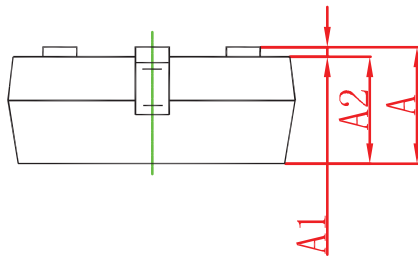
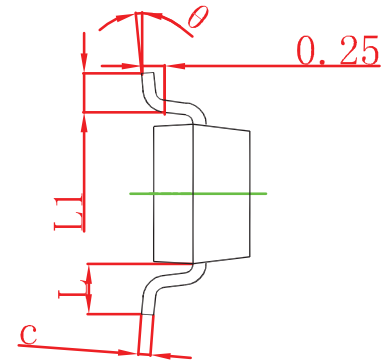
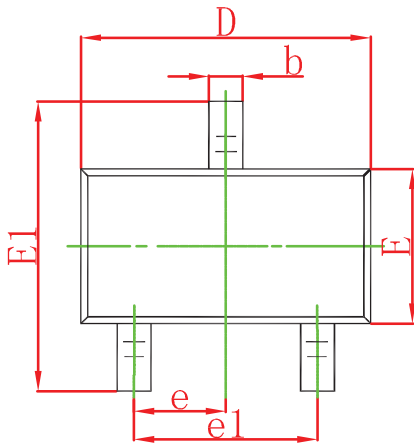


C_O — V_R



P_D — T_a





Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
A	0.900	1.150	0.035	0.045
A1	0.000	0.100	0.000	0.004
A2	0.900	1.050	0.035	0.041
b	0.300	0.500	0.012	0.020
c	0.080	0.150	0.003	0.006
D	2.800	3.000	0.110	0.118
E	1.200	1.400	0.047	0.055
E1	2.250	2.550	0.089	0.100
e	0.950 TYP		0.037 TYP	
e1	1.800	2.000	0.071	0.079
L	0.550 REF		0.022 REF	
L1	0.300	0.500	0.012	0.020
θ	0°	8°	0°	8°